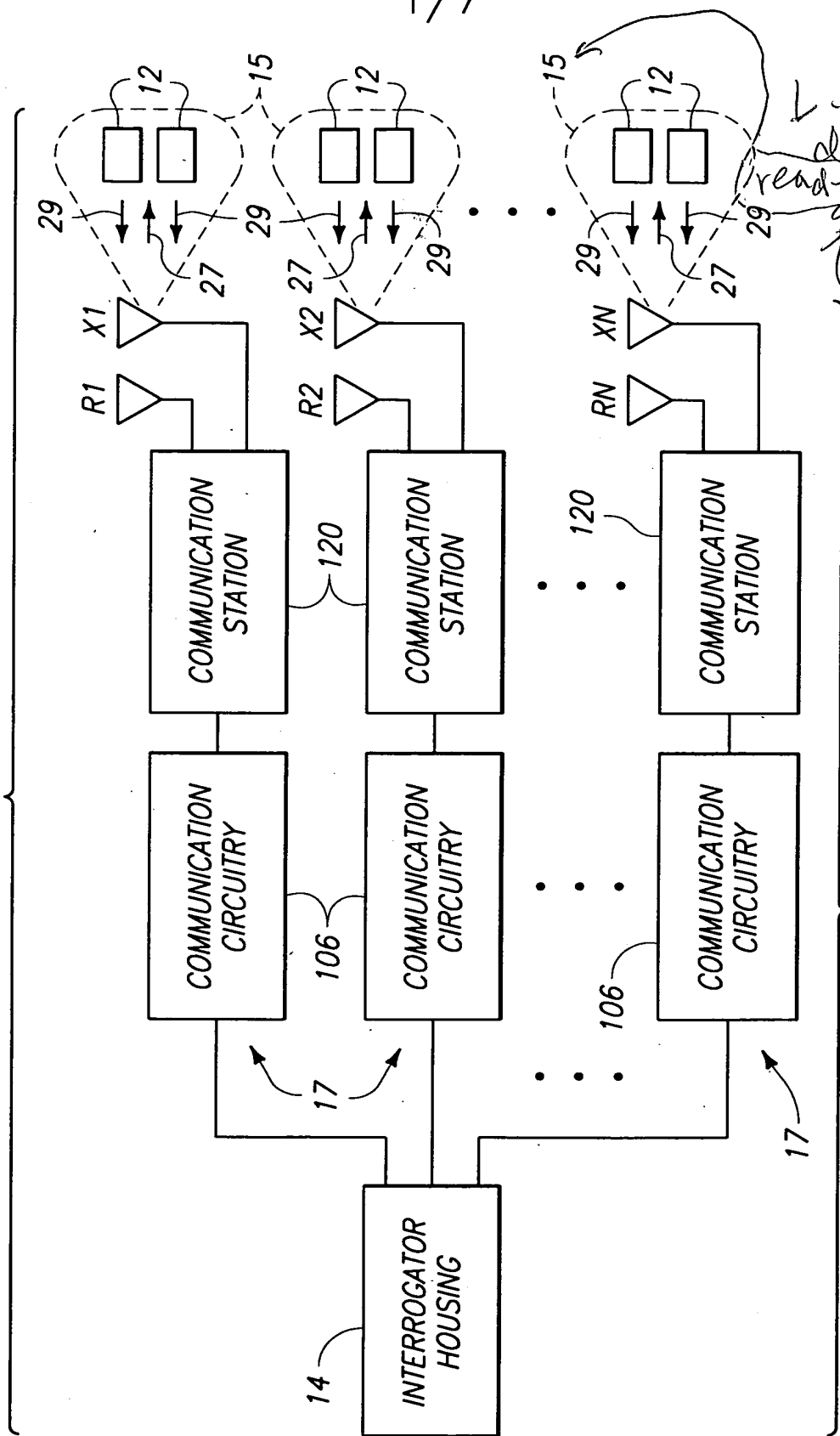


1/7

66000" 209960

*return link*  
*communications system*  
10



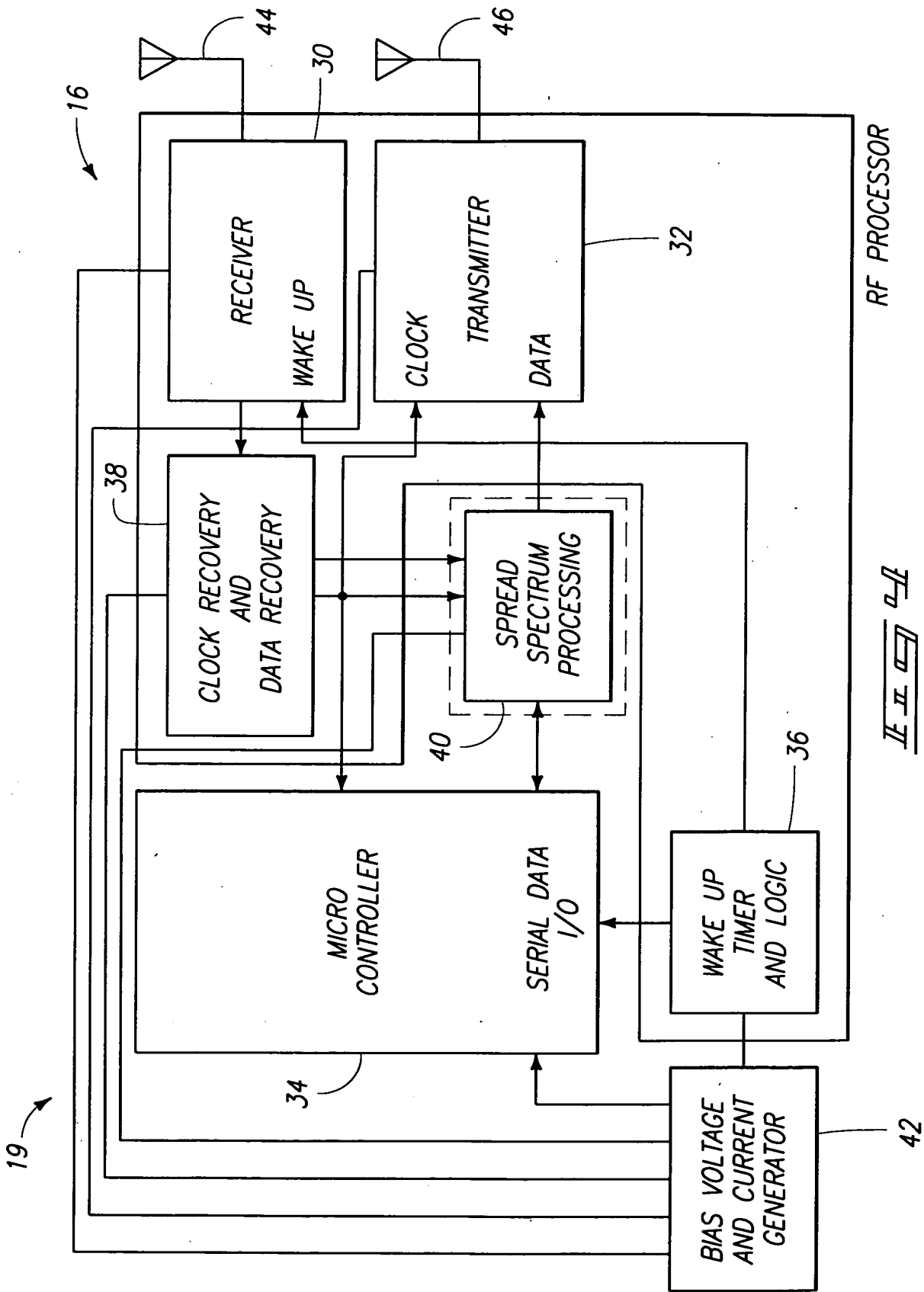
*logic devices*  
*read-zone*  
*forward link*

26  
*interrogator*

II II II



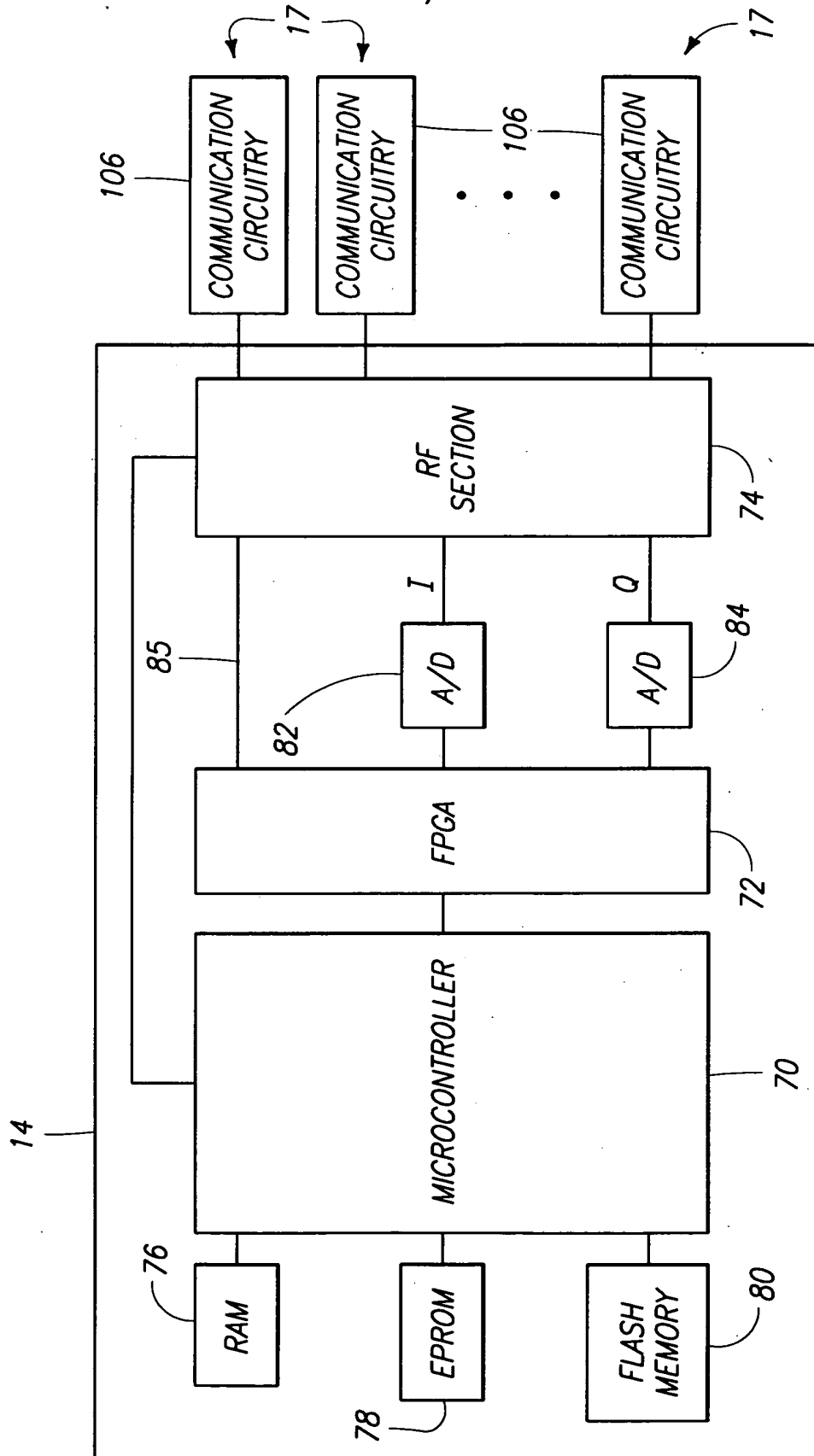
3/7



RF PROCESSOR

FIG. 3

4/7



## COMMUNICATION CIRCUITRY

# COMMUNICATION CIRCUITRY

COMMUNICATION  
CIRCUITRY

CIRCUITRY

• 106

17

# RF SECTION

## SECTION

74



Q

A/D

A/D

84

**FPGA**

72

## MICROCONTROLLER

70

**RAM**

2

**EPROM**

78

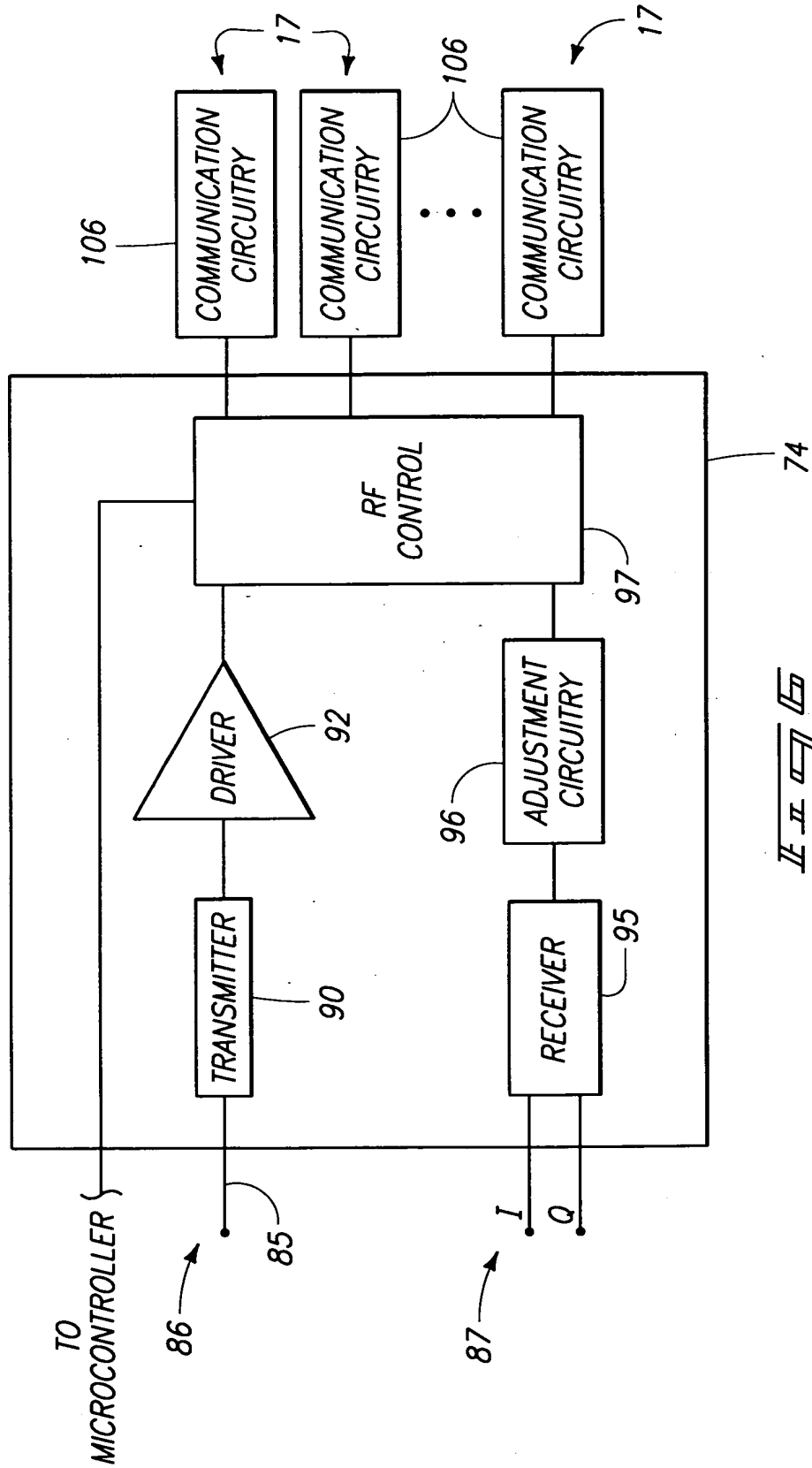
FLASH  
MEMORY

# MEMORY

80

565

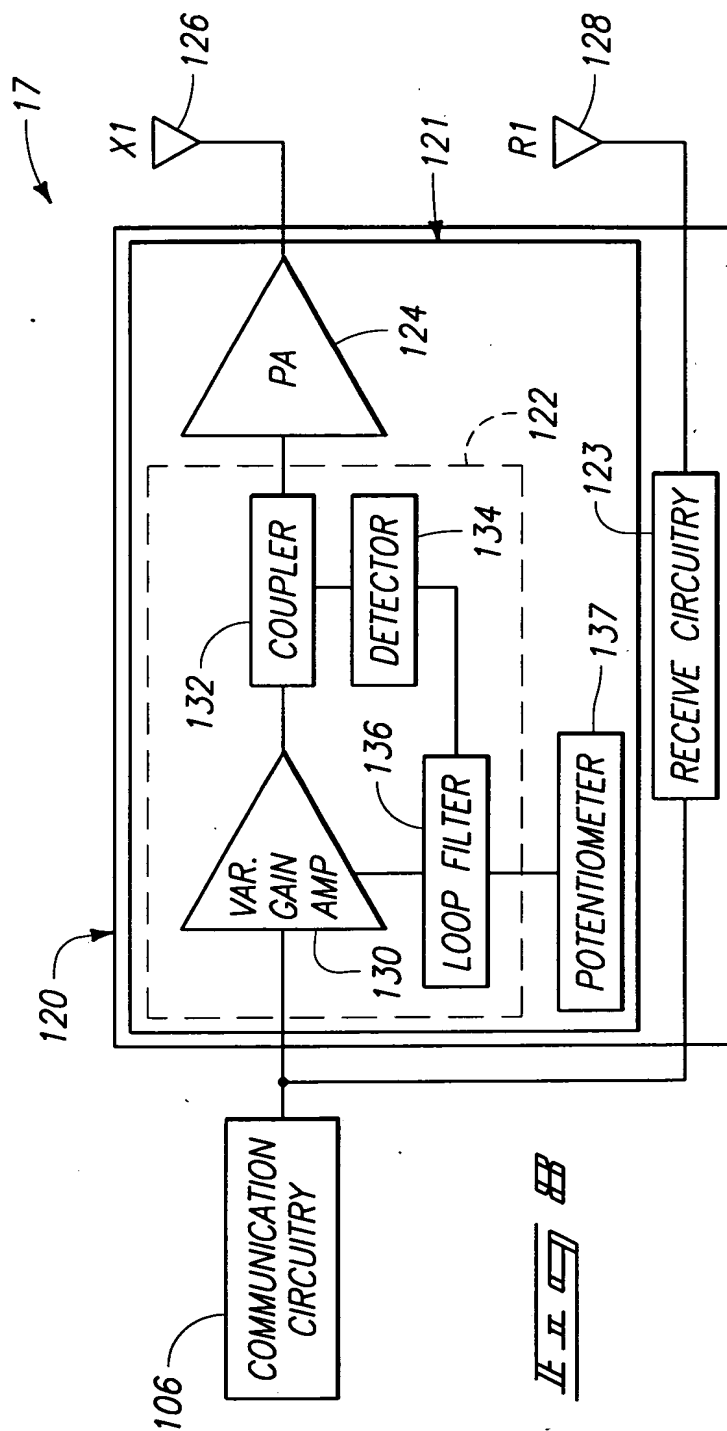
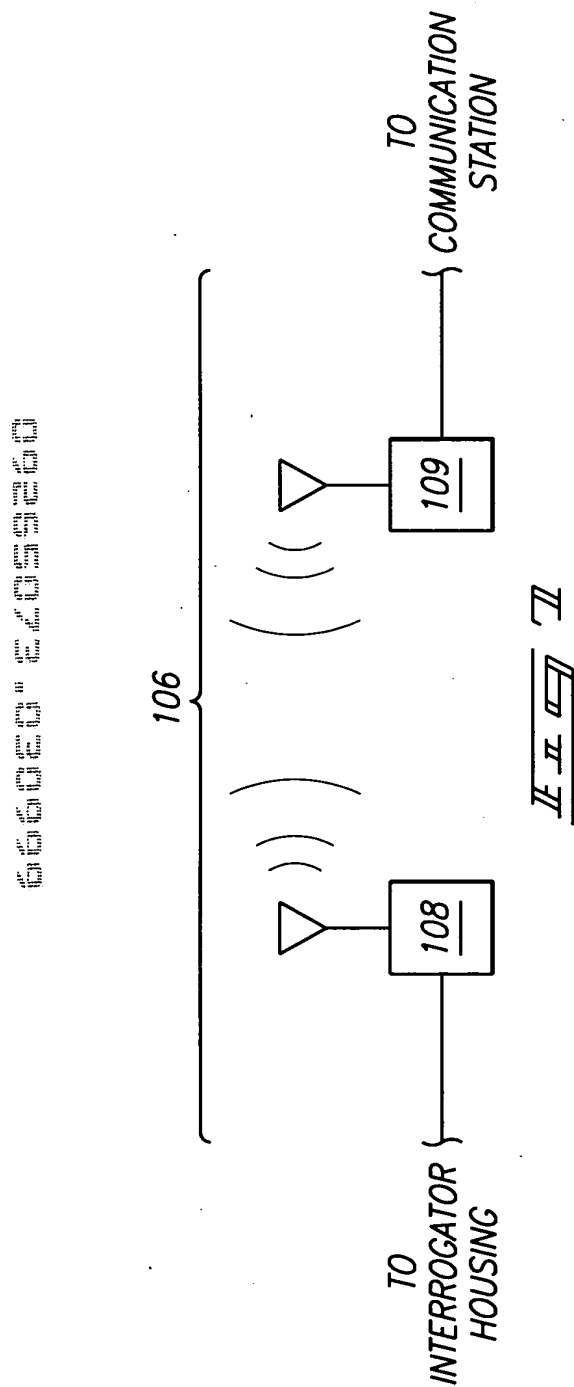
5/7



66000 205000

11 11 11 11

6/7



7/7

